

AMENDMENTS TO THE SPECIFICATION

Page 5, [0028], please amend the original paragraph as follows:

FIG. 6 is the many- $V_t$  version of FIG. 3. Resistor [[402]] 420 corresponds to resistor 112 and diodes 422-1 to 422-N2 replace diode 108. Hence, node A goes to  $(N2 * V_t)$  above ground, where "N2" is a positive integer.

Page 6, [0031], please amend the original paragraph as follows:

[[In]] FIG. 5 has another aspect of the present invention showing the use of more than one series coupled diode-connected transistors on each input of the comparator, i.e., diode-connected transistors 322-1 to 322-N1 on node A and diode-connected transistors ~~322-1 to 322-M1~~ 324-1 to 324-M1 on node B, where N1 and M1 are positive numbers. The circuit of FIG. 5 with its several series connected transistors at both the A and B nodes, causes the voltages at these nodes to be greater than one  $V_t$ , and causes a shift at the point the PUR signal fires as compared to the design shown in FIG. 1.